



SHAHAJIRAO PATIL VIKAS PRATISHTHAN
S. B. PATIL COLLEGE OF ENGINEERING, INDAPUR, DIST: PUNE.
 DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING

ACA - R - 07	CLASS TIME TABLE		Academic Year: 2019-20
Rev : 01			
Date: 12.06.2015			Semester: I

Ref: SPVP/SBPCOE/E&TC/2019-20/F2/01

"NAAC Accredited"

Date: 12.06.2019

Class: BE

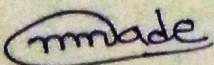
Wef: 15.06.2019

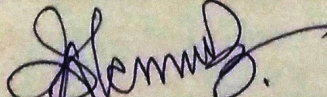
Name of Class Teacher: Prof. P. S. TOGRIKAR

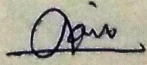
Class Room Location : 122

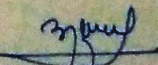
Time/Day	8.30 am- 9.30 am	9.30 am- 10.30 am	10.30 am- 11.30 am	11.30 am to 12.15 pm	12.15 pm- 1.15 pm	1.15 pm- 2.15 pm	02.15 pm to 2.30 pm	2.30 pm -3.30 pm	3.30 pm -4.30 pm
MON	VLSI (PR)(120)		IOT (119A)		VLSI(122)	RMT (122)		EIA(122)	CNS(122)
TUE	CNS(PR)(119)		RMT(122)		VLSI(122)	EIA (122)		CNS(122)	IOT (119A)
WED	EIA(122)	IOT (119A)	VLSI(122)		RMT(PR)(121)			CNS(122)	RMT(122)
THU	IOT(PR)(119A)		RMT(122)		CNS(122)	VLSI(122)		EIA(122)	IOT (119A)
FRI	IOT(119A)	RMT(122)	CNS(122)		PROJECT			VLSI(122)	EIA(122)
SAT	IOT(122)	TUTOR MEET	CNS(122)		RMT (119A)	EIA (122)		PROJECT	

ABBR:	Name of Subject	Name of Subject Teacher	ABBR:	Name of Subject	Name of Subject Teacher
IOT	INTERNET OF THINGS(TH+PR)	PROF.A.S.ZADBUKE	EIA	ELECTRONICS IN AGRICULTURE(TH+TUT)	Prof.P.S.TOGRIKAR
RMT	RADIATION & MICROWAVE	Prof.C.V. Nalawade	CNS	COMPUTER NETWORK	PROF.M.M.ZADE
VLSI	VLSI DESIGN & TECHNOLOGY (TH+PR)	Prof.V.U.BANSUDE	PW	Project Work	Respective Guide


 Prof.M.M.Zade
 Time Table I/C


 Prof. Y.S. Bahendwar
 Head of the Department


 Prof.S.R.Kshirsagar
 Dean Academics


 Dr.P.D.Nemade
 Principal

Notes - 10 minutes in the 1st Lecture/practical will be for yoga.